

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A method for analyzing performance of an electrical design, comprising:

receiving a plurality of package files, wherein each package file is descriptive of a portion an electrical attribute associated with a plurality of components of the electrical design and defines a reference connection;

selectively and logically linking first and second package files of the plurality of package files by correlating respective reference connections of each of the first and second package files; and

processing modeling travel of an electrical signal using the logically linked first and second package files to determine a performance characteristic of the electrical design.

2. (Original) The method of claim 1, further comprising determining a second performance characteristic by selectively modifying contents of the first package file while preserving contents of the second package file.

3. (Original) The method of claim 1, wherein selectively and logically linking the first and the second package files further includes linking the first and the second package files according to configuration file instructions.

4. (Original) The method of claim 1, wherein receiving the plurality of package files further includes generating the package files from a group of data consisting of at least one of: logical data, physical data and electrical data.

5. (Original) The method of claim 1, further comprising separately storing each package file.

6. (Original) The method of claim 1, wherein selectively and logically linking the first and the second package files further includes generating a net file having characteristics of both the first and the second package files.

7. (Original) The method of claim 6, further comprising selectively and logically linking the net file with another net file.

8. (Original) The method of claim 1, wherein receiving the plurality of package files further includes receiving an incomplete package file.

9. (Original) The method of claim 1, wherein correlating respective reference connections of each of the first and second package files further includes correlating a connection selected from a group consisting of at least one of: a connection pin, another physical data point, a logical data point and an electrical data point.

10. (Original) The method of claim 1, further comprising assigning an electrical characteristic to a portion of the first package file.

11. (Original) The method of claim 1, further comprising selectively modifying a portion of the first package file while preserving a remaining portion of the first package file.

12. (Original) The method of claim 1, wherein correlating respective reference connections of each of the first and second package files further includes programmatically locating the reference connections.

13. (Currently Amended) The method of claim 1, wherein modeling the travel of the electrical signal using processing the logically linked first and second package files further includes generating an output selected from a group consisting of at least one of: an analog waveform, noise data and timing data.

14. (Original) The method of claim 1, further comprising using the first package file in a subsequent simulation.

15. (Currently Amended) An apparatus, comprising:

a memory;

a database resident in the memory, the database storing a plurality of package files, each package file of the plurality of package files being descriptive of a portion an electrical attribute associated with a plurality of components of an electrical design and having a reference connection correlating to a respective reference connection of another package file of the plurality; and

a program for selectively stitching first and second package files of the plurality of package files by correlating the respective reference connections of the first and second package files; the program being further configured to initiate processing of modeling travel of an electrical signal using the stitched first and second package files to determine a performance characteristic of the electrical design.

16. (Original) The apparatus of claim 15, wherein the program initiates determining a second performance characteristic by selectively modifying contents of the first package file while preserving contents of the second package file.

17. (Original) The apparatus of claim 15, wherein the program initiates stitching the first and the second package files according to configuration file instructions.

18. (Original) The apparatus of claim 15, wherein the program initiates generating the plurality of package files from a group of data consisting of at least one of: logical data, physical data and electrical data.

19. (Original) The apparatus of claim 15, wherein the program initiates separately storing each package file.

20. (Original) The apparatus of claim 15, wherein the program initiates generating a net file having characteristics of both the first and the second package files.

21. (Original) The apparatus of claim 20, wherein the program initiates selectively and logically linking the net file with another net file.

22. (Currently Amended) The apparatus of claim 15, wherein the generation of the first package file is incomplete as the modeling processing initiates.

23. (Original) The apparatus of claim 15, wherein the program initiates correlating a connection selected from a group consisting of at least one of: a connection pin, another physical data point, a logical data point and an electrical data point.

24. (Original) The apparatus of claim 15, wherein the program initiates assigning an electrical model to a portion of the first package file.

25. (Original) The apparatus of claim 15, wherein the program initiates selectively modifying a portion of the first package file while preserving a remaining portion of the first package file.

26. (Original) The apparatus of claim 15, wherein the program automatically initiates locating the reference connections.

27. (Original) The apparatus of claim 15, wherein the program initiates locating the reference connections according to user input.

28. (Original) The apparatus of claim 15, wherein the program initiates generating an output selected from a group consisting of at least one of: an analog waveform, noise data and timing data.

29. (Original) The apparatus of claim 15, wherein the program initiates reusing the first package file in a subsequent simulation.

30. (Currently Amended) A program product, comprising:

a program configured to selectively stitch first and second package files of a plurality of package files by correlating respective reference connections of the first and second package files, wherein each package file is descriptive of a portion an electrical attribute associated with a plurality of components of an electrical design; the program being further configured to initiate processing of modeling travel of an electrical signal using the stitched first and second package files to determine a performance characteristic of the electrical design; and

a signal bearing medium bearing the first program.

31. (Original) The program product of claim 30, wherein the signal bearing medium includes at least one of a recordable medium and a transmission-type medium.